Notice of References Cited

				
Application/Control No.		applicant(s)/Patent Under		
09/816,926	¢	Reexamination ARNOLD, RALF		
Examiner		Art Unit		
Shane F Gerstl		2183	Page 1 of 1	

U.S. PATENT DOCUMENTS

* `		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,077,315	06-2000	Greenbaum et al.	717/157
	В	US-6,023,742	02-2000	Ebeling et al.	710/107
	C	US-			
	D	US-			
	Е	US-			
	F	US-			
	G	US-			
	Н	US-			
	-	US-			
	J	US-		·	
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	°EP 637173 A3	04-1995	European Patent	KIMURA et al.	G06F 15/20
	0					
	Р					
	Q					
	R	·				
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	o Andre DeHone; Transit Note #118 Notes on Coupling Processors with Reconfigurable Logic; March 1995; Page 23
	٧	John L Hennessy and David A Patterson; Computer Organization and Design The Hardware/Software Interface; Morgan Kaufman Publishers; 1998; Pages 351 and B-9
	w	
	х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.